

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with J. Oriti on 6/3/08.

The application has been amended as follows:

In claim 1, line 9, "the other" has been changed to --each other--;

Line 15, "the other" has been changed to --each other--.

In claim 7, line 7, "the other" has been changed to --each other--;

Line 12, "the other" has been changed to --each other--.

2. The following is an examiner's statement of reasons for allowance: The prior art of the record fails to disclose the processor and the peripheral each generating a random value, performing a first operation based on the random value thereof to produce an intermediary value, and sending the produced intermediary value to the other by way of the path; the peripheral generating a signed representation of the intermediary value and sending the signed representation to the processor by way of the path, the signed representation being verifiable by the processor; the processor and the peripheral each performing a second operation based on the intermediary value from the other and also based on the random value thereof to produce a final value, the final value as produced by the processor being equal to the final value as produced by

the peripheral and thus constituting a shared secret known only to the processor and the peripheral in combination of other limitations recited in claims 1 and 7; the processor retrieving the data from a trusted section of a memory of the computing device, encrypting such data according to the content key KC, performing the MAC algorithm over the encrypted trusted data according to the MAC key KM to produce MAC data, storing the encrypted trusted data in a non-trusted section of the memory, and storing the MAC data in such non-trusted section of the memory; the processor storing information regarding how to access such stored encrypted trusted data and MAC data in the memory as a transfer descriptor in the non-trusted section of the memory, and then providing the peripheral by way of the path with a physical address of such transfer descriptor in such memory; the peripheral accessing the transfer descriptor in the memory at the physical address thereof by way of the path, reviewing the information in the transfer descriptor, and retrieving the stored encrypted trusted data and MAC data in the memory based on the information and by way of the path in combination of other limitation recited in claims 12 and 13.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to AJIT G. PATEL whose telephone number is 571-272-3140. The examiner can normally be reached on MONDAY- FRIDAY.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Doris TO can be reached on 571-272-7629. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/William Trost/
Supervisory Patent Examiner, Art Unit 2616

/AJIT G. PATEL/
Primary Examiner, Art Unit 2616